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Applicant:

Aronson et al.

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For:

FLEXIBLE CIRCUIT DESIGN FOR IMPROVED LASER BIAS

CONNECTIONS TO OPTICAL SUBASSEMBLIES

SUPPLEMENTAL INFORMATION DISCLOSURE CITATIONS MADE BY APPLICANT

U.S. Patent Documents

Examiner

Document

Initial*

Number

Date

Name

DT Al.

4,953,006

08/28/1990

Kovats et al.

Foreign Patent Documents

Examiner

Document

Publ.

Country or

Sub

Trans-

Initial*

Number

<u>Date</u>

Patent Office

Class

Class

lation

NONE

Other Documents

(including author), title, relevant pages, date of publication including at least month and year).

Examiner

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NONE

Examiner:

/Dzung Tran/

Date Considered:

10/04/2006

^{*}EXAMINER: Initial if reference considered, whether or not citation is in conformance with MPEP 609; draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant.